



Docket No.: 50090-470

RECEIVED PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

In re Application of

Masatoshi YASUNAGA

Serial No.: 10/057,923

Filed: January 29, 2002

For: SEMICONDUCTOR DEVICE

Group Art Unit: 2814

Examiner: D. Nguyen

#8/Amend B
Ardaud
1/30/03

AMENDMENT

Commissioner for Patents
Washington, DC 20231

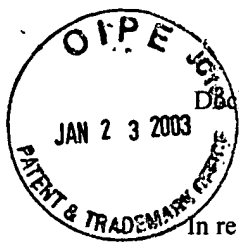
Sir:

The following Amendment and Remarks are submitted in response to the Office
Action dated October 23, 2002.

IN THE CLAIMS:

Please amend claims 1 and 3 as follows.

1. (Twice Amended) A semiconductor device comprising:
- a substrate;
 - a semiconductor chip mounted on the substrate;
 - external electrodes provided on the back of the substrate, for connecting electrodes of the semiconductor chip to the outside;
 - a sealing member encapsulating the semiconductor chip on the substrate; and
 - a heat sink plate fixed by the sealing member, wherein



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THE COMMISSIONER FOR PATENTS AND TRADEMARKS
Washington, DC 20231

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

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No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	6	20	0	\$18.00 =	\$0.00
Independent Claims	3	3	0	\$84.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
Total of Above Calculations					\$0.00

☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: January 23, 2003

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